

APR 22 2005


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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/666,220	
	Filing Date	Sept. 19, 2003	
	First Named Inventor	Hyun-Jin Cho	
	Art Unit	2818	
	Examiner Name	Tran, Mai Huong C	
Total Number of Pages in This Submission	5	Attorney Docket Number	C002-D-2

ENCLOSURES (Check all that apply)		
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<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	The Commissioner is authorized to charge any fees in this submission to deposit account 50-2538 (docket number C002-D-2)	
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Firm or Individual	Hark C. Chan, Reg. No. 35,477	
Signature		
Date	April 22, 2005	

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Hyun-Jin Cho, et al.)	Examiner: Tran, Mai Huong C
)	
Serial No.: 10/666,220)	Group Art Unit: 2818
)	
Filed: September 19, 2003)	
)	
For: Stability in Thyristor-Based Memory Device)	
)	

SUBMISSION OF DOCUMENTS REQUESTED BY EXAMINER

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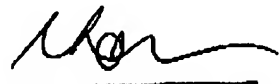
Sir:

On April 16, 2005, three documents requested by the Examiner were submitted. The undersigned recently obtained a better quality version of one of the documents. This document is hereby resubmitted to facilitate the review by the Examiner.

The Commissioner is authorized to charge any fees in this submission to deposit account 50-2538 (docket number C002-D-2)

Date: April 22, 2005

Respectfully submitted,


Hark C. Chan (Reg. No. 35,477)